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低电压检测 – LVD说明

该功能使能用于监测电源电压 **VDD**，或 **LVDIN** 输入电压，若电源电压低于一定值可提供一个警告信号。此功能在电池类产品中非常有用，在电池电压较低时产生警告信号。低电压检测也可产生中断信号。

example 说明

此范例演示了 `Lvd.h` 相关接口的使用

程序说明

1. 配置低电压侦测的电压值，初始化 `LvdIsDetected` 作为状态指示
2. 使能低电压检测功能
3. 轮询检测低电压flag，若flag置位，说明 `VDD` 电压低于设定的电压值

现象说明

1. 连接 `e-link` 与目标板，使用外部电源供电5V直接给VDD
2. 编译并下载程序
3. 缓慢调整外部电源电压 `<3.0V`，此时通过变量监视窗口可查看到 `LvdIsDetected` 值修改为 `true`。

FAQ

1. 为什么开启LVD后需要delay一段时间

LVD会与MCU内部的电路电压进行比较，开启后内部电路可能还没有完全稳定，若立即读取可能会造成误判，因此建议开启后delay一小段时间后再进行判断

delay时间详细可参考规格书LVD的电气特性部分的描述。