

Pin-to-pin connection diagram for CN4 and CN3. The diagram shows the following connections:

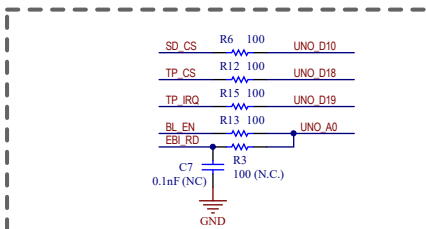
- Correct Connections (Red X):**
 - CN4 IOREF to CN3 D19/SC_L
 - CN4 RESET to CN3 D18/SDA
 - CN4 3.3V to CN3 A18/SD
 - CN4 5V to CN3 GND
 - CN4 VIN to CN3 D13/SC_K
 - CN4 E_O/A0 to CN3 D12/MISO
 - CN4 E_WR/A1 to CN3 D11/MOSI
 - CN4 E_A/A2 to CN3 D10/SS
 - CN4 E_A/A3 to CN3 D9/D1
 - CN4 E_CS/A3 to CN3 D8/D0
 - CN4 SD/A4 to CN3 D7/D17
 - CN4 SCL/A5 to CN3 D6/D6
 - CN4 SCL/A5 to CN3 D5/D5
 - CN4 SCL/A5 to CN3 D4/D4
 - CN4 SCL/A5 to CN3 D3/D3
 - CN4 SCL/A5 to CN3 D2/D2
 - CN4 SCL/A5 to CN3 D1/TX
 - CN4 SCL/A5 to CN3 D0/Rx
- Incorrect Connections (Blue X):**
 - CN4 IOREF to CN3 D13/SC_K
 - CN4 RESET to CN3 D12/MISO
 - CN4 3.3V to CN3 D11/MOSI
 - CN4 5V to CN3 D10/SS
 - CN4 VIN to CN3 D9/D1
 - CN4 E_O/A0 to CN3 D8/D0
 - CN4 E_WR/A1 to CN3 D7/D17
 - CN4 E_A/A2 to CN3 D6/D6
 - CN4 E_A/A3 to CN3 D5/D5
 - CN4 E_CS/A3 to CN3 D4/D4
 - CN4 SD/A4 to CN3 D3/D3
 - CN4 SCL/A5 to CN3 D2/D2
 - CN4 SCL/A5 to CN3 D1/TX
 - CN4 SCL/A5 to CN3 D0/Rx

Additional components and labels:

- Resistors:** R27 (3.3V) connected between GND and UNO D12; 100 ohm resistor connected between GND and UNO D10.
- Labels:** CN4_LEFT, @BM53A367A, The A4 and D18 is the same NET, The A5 and D19 is the same NET, CN_RIGHT.

SPI Mode $\Rightarrow$ IM0 is LOW, U4_OEn equal to V_IOREF, U4 Output Disable.

EBI Mode $\Rightarrow$ IM1 is HIGH, IM0 is HIGH, U4_OEn equal to 0V, U4 Output Enable.

[illegible][illegible]

Pin Header (Left):

- 1 X-
- 2 Y-
- 3 X+
- 4 Y+
- 5 GND
- 6 VDDI
- 7 VDD
- 8 FMARK
- 9 CS/SPI_CS
- 10 DC/SPI_SCK
- 11 WR/SPI_DC
- 12 RD
- 13 SPI_SDI
- 14 SPI_SDO
- 15 RESET
- 16 GND
- 17 DB0
- 18 DB1
- 19 DB2
- 20 DB3
- 21 DB4
- 22 DB5
- 23 DB6
- 24 DB7
- 25 3V UNO D8
- 26 3V UNO D9
- 27 3V UNO D2
- 28 3V UNO D3
- 29 3V UNO D4
- 30 3V UNO D5
- 31 3V UNO D6
- 32 3V UNO D7
- 33 LED-A
- 34 LED-K
- 35 LED-K
- 36 LED-K
- 37 LED-K
- 38 IM0
- 39 IM1
- 40 IM2

Circuit Components and Connections:

- 3V Regulator (U3):** VDD to pin 3, OUT to pin 1, GND to pin 2. Input is 5V from pin 9 through a 100k resistor (R4).
- Capacitor (C4):** 0.1uF, connected between pin 6 and GND.
- Reset Circuit:** Pin 15 (RESET) is connected to pin 1 of U3 and to a switch (SW1) through a 100k resistor (R14). The switch is connected to GND.
- Backlight Control:** Pin 33 (LED-A) is connected to a 5V regulator (Q2) through a 10k resistor (R8). The 5V regulator is connected to GND. A 10k NTC resistor (R5) is connected between pin 33 and a 3V supply.
- Other Connections:** Pin 32 is connected to GND. Pin 39 is connected to GND through a 100k resistor (R16).

Backlight Control Logic Table:

R5	R8	BackLight
NC	NC	Always ON
Fw	Fw	Controlled UNO A0

Module Label: Z208I1009

R5	R8	BackLight
Fit	NC	Always ON
NC	Fit	Controlled UNO A0

Title:	BMD58T280
Design by:	BESTMODULES TEAM
Data:	2023/4/20
Rev:	V1.0

