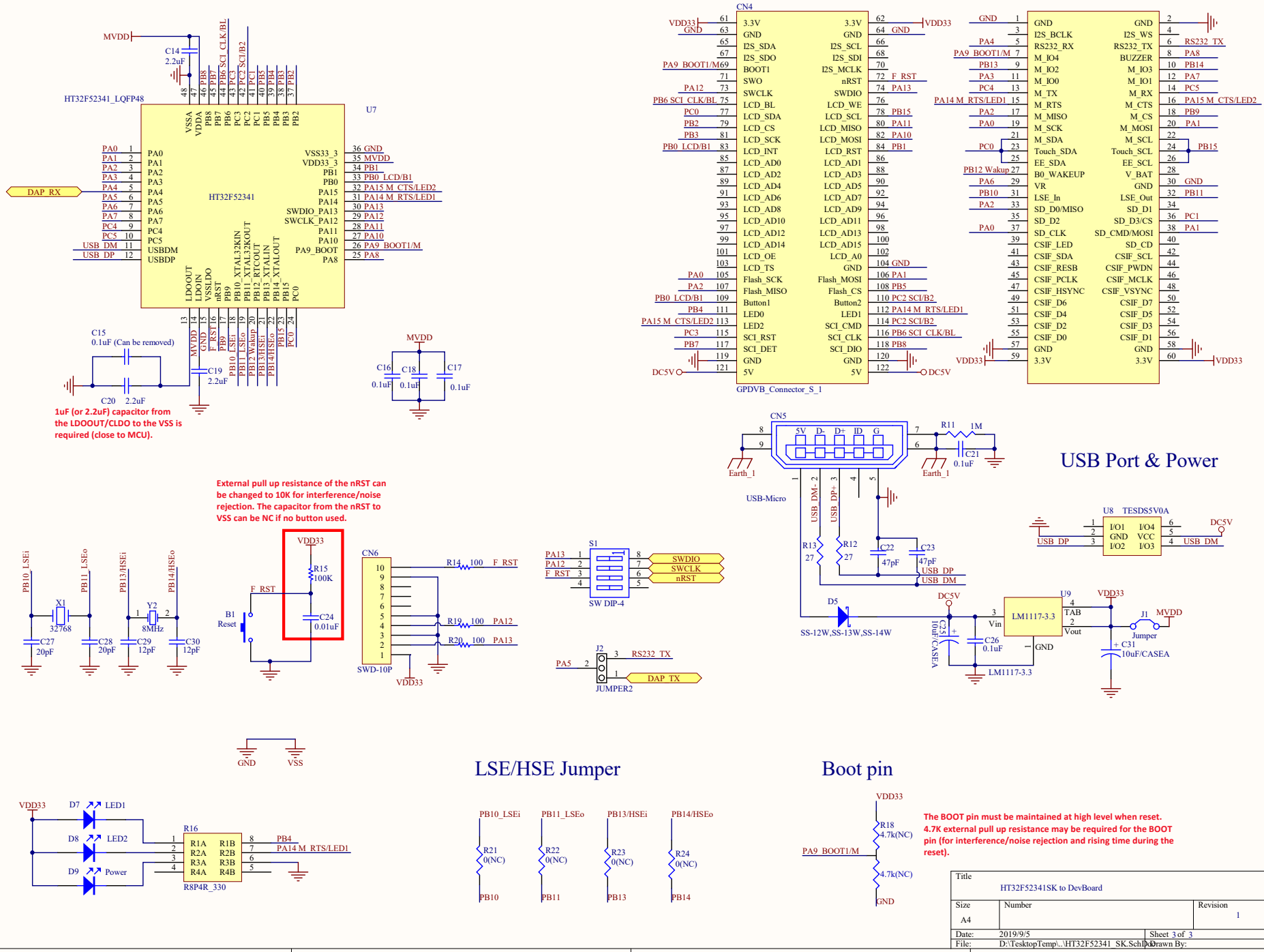
HT32F52341_LQFP48
U7
HT32F52341
MVDD
C14 2.2uF
DAP_RX
USB_DM
USB_DP
C15 0.1uF (Can be removed)
C19 2.2uF
C20 2.2uF
1uF (or 2.2uF) capacitor from the LDOOUT/CLDO to the VSS is required (close to MCU).
C16 0.1uF
C18 0.1uF
C17 0.1uF
CN4
GPDVB_Connector_S_1
External pull up resistance of the nRST can be changed to 10K for interference/noise rejection. The capacitor from the nRST to VSS can be NC if no button used.
X1 32768
Y2 8MHz
C27 20pF
C28 20pF
C29 12pF
C30 12pF
B1 Reset
R15 100K
C24 0.01uF
CN6
SWD-10P
S1
SW DIP-4
SWDIO
SWCLK
nRST
J2
JUMPER2
RS232_TX
DAP_TX
CN5
USB-Micro
R11 1M
C21 0.1uF
USB Port & Power
U8 TESDS5V0A
R13 27
R12 27
C22 47pF
C23 47pF
D5
SS-12W,SS-13W,SS-14W
U9
LM1117-3.3
C25 10uF/CASEA
C26 0.1uF
J1
Jumper
C31 10uF/CASEA
GND
VSS
LSE/HSE Jumper
PB10_LSEi
PB11_LSEo
PB13/HSEi
PB14/HSEo
R21 0(NC)
R22 0(NC)
R23 0(NC)
R24 0(NC)
Boot pin
PA9_BOOT1/M
R18 4.7k(NC)
4.7k(NC)
The BOOT pin must be maintained at high level when reset. 4.7K external pull up resistance may be required for the BOOT pin (for interference/noise rejection and rising time during the reset).
D7 LED1
D8 LED2
D9 Power
R16
R8P4R_330
Title
HT32F52341SK to DevBoard
Size
A4
Number
Revision
1
Date: 2019/9/5
Sheet 3 of 3
File: D:\TesktopTemp\...\HT32F52341_SK.SchDoc
Drawn By: