

[illegible]

Figure 1 shows two pin connection diagrams for the SN74LVC1T45. The top diagram shows the J8 connector with VDD\_clink32 and VDD\_TargetBoard connected to pins 1 and 2, and pin 3 connected to pin 4. The bottom diagram shows the U6 connector with VDD\_clink32 and VDD\_TargetBoard connected to pins 1 and 2, and pin 3 connected to pin 4. The U6 connector also shows pins 5 and 6 connected to pins 7 and 8, and pin 9 connected to pin 10.

**B1 to B7**  
**C14 to C50**  
**CN4 to CN7**  
**D5 to D50**  
**J1 to J7**  
**R11 to R50**  
**S1 to S7**  
**U7 to U20**  
**X1 , Y2**

The name of PCB HT32F52354_SK.PrjPcb	Size HT A4	Cannot open file E:\New HT 32 Logo.bmp
Title e-Link32 Lite CMSIS DAP	Rev. V2.3	
Date: 2017/7/12	Sheet 1 of 2	
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